IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):   Mark S. Moir et al.

Title: OBSTRUCTION-FREE SYNCHRONIZATION FOR SHARED DATA STRUCTURES

Application No.:   10/620,748   Filed: July 16, 2003

Examiner: Mano Padmanabhan   Group Art Unit: 2188

Atty. Docket No.:   004-8252

September 27, 2004

Mail Stop Amendment
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☒   Form(s) PTO-1449 (2 pages), including copy(ies) of 33 reference(s).
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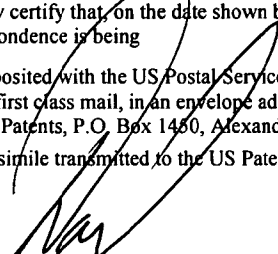
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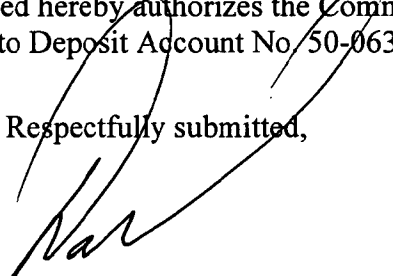
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☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.

☒ The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.
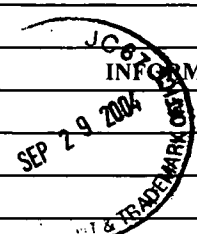
If however, this Information Disclosure Statement is filed after the period specified in § 1.97(b), the undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. 50-0631.

Respectfully submitted,

David W. O'Brien, Reg. No. 40,107
Attorney for Applicant(s)
(512) 338-6314
(512) 338-6301 (fax)

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David W. O'Brien   Date

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U.S. Department of Commerce, Patent and Trademark Office			Attorney Docket No.:	004-8252
			Application No.:	10/620,748
INFORMATION DISCLOSURE STATEMENT BY APPLICANT			Applicant(s):	Moir et al.
(Use several sheets if necessary)			Filing Date:	July 16, 2003
			Group Art Unit:	2188
			Date Submitted:	September 27, 2004

NON PATENT LITERATURE DOCUMENTS

*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
	1	Afek, Yehuda et al., "*Long-Lived Renaming Made Adaptive*", 18th Annual ACM Symposium on Principles of Distributed Computing, pages 91-104, 1999.
	2	Afek, Yehuda, "*Wait-Free Made Fast*", 27th Annual ACM Symposium on Theory of Computing, pages 538-547, 1995.
	3	Agesen, Ole et al., "*DCAS-Based Concurrent Deques*", 12th Annual ACM Symposium on Parallel Algorithms and Architectures, pages 137-146, July 2000.
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	7	Barnes, Greg, "*A Method for Implementing Lock-Free Shared Data Structures*", 5th Annual ACM Symposium on Parallel Algorithms and Architectures, pages 261-270, 1993.
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	14	Herlihy, Maurice, "*Dynamic-Sized Lockfree Data Structures*", Sun Microsystems Technical Report SMLI TR-2002-112, June 2002.
	15	Herlihy, Maurice et al., "*Linearizability: A Correctness Condition for Concurrent Objects*", ACM Transactions on Programming Languages and Systems, pages 463-492, July 1990.
	16	Herlihy, Maurice et al., "*The Repeat Offender Problem: A Mechanism for Supporting Dynamic-Sized Lock-Free Data Structures*", Sun Microsystems Technical Report SMLI TR-2002-112, June 2002.

Examiner	/Sheng Jen Tsai/	Date Considered	04/29/2008

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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Form 1449B

U.S. Department of Commerce, Patent and Trademark Office	Attorney Docket No.:	004-8252
	Application No.:	10/620,748
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Applicant(s):	Moir et al.
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NON PATENT LITERATURE DOCUMENTS

*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
	17	Herlihy, Maurice et al., "*Transactional Memory: Architectural Support for Lock-Free Data Structures*", 20th International Symposium in Computer Architecture, 1993.
	18	Herlihy, Maurice et al., "*Obstruction-Free Synchronization: Double-Ended Queues as an Example*", 23rd International Conference on Distributed Computing, May 2003.
	19	Israeli, Amos et al., "*Disjoint-Access-Parallel Implementations of Strong Shared Memory Primitives*", 13th Annual ACM Symposium on Principles of Distributed Computing, pages 151-160, 1994
	20	Lamport, Leslie, "*How to Make a Multiprocessor Computer that Correctly Executes Multiprocess Programs*", IEEE Transactions on Computers, September 1979.
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	26	Moir, Mark, "*Laziness Pays! Using Lazy Synchronization Mechanisms to Improve Non-Blocking Constructions*", 19th Annual ACM Symposium on Principles of Distributed Computing, 2000.
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	29	Moir, Mark et al., "*Wait-Free Algorithms for Fast, Long-Lived Renaming*", Science of Computer Programming, August 1994.
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Examiner	/Sheng Jen Tsai/	Date Considered	04/29/2008

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